



INDIAN INSTITUTE OF TECHNOLOGY PATNA
BIHTA PATNA-801106
RESEARCH & DEVELOPMENT UNIT

ADVERTISEMENT NO: R&D/624/SERB/232**DATED: 13.06.2022**

Project No R&D/SP/EE/SERB/2021-22/624

Applications are invited in the prescribed format only for the following assignment in a purely time bound research project undertaken in this institute.

1. (a) Name of the temporary assignment : **Junior Research Fellow (JRF)**
(b) Number of Post : **01 (one)**
(c) Duration of the Post : **Initially for one year, extendable to another two years subject to satisfactory performance.**
2. Name of the temporary research project: **“Design and Development of Silicon Artificial Neuron and Synapse for Brain-Inspired Computing Architectures”.**
3. Name of the sponsoring Agency : **DST, SERB**
4. Consolidated Fellowship/Salary : **Rs. 31,000/- per month plus 8% HRA as per GoI rule**
5. Qualifications & Experience : **B.Tech./B.E./M.Tech./ME/ Equivalent Degree in Engineering and Technology with specialization in VLSI, Microelectronics and Semiconductor Devices**

The candidate desirably should have **B.Tech./ B.E./ M.Tech./ ME/ Equivalent Degree in Engineering and Technology with specialization in VLSI, Microelectronics and Semiconductor Devices or Equivalent** with a minimum CPI of 6.5 or 60 % of marks. The upper age limit for applying for the JRF position shall be 32 years. Relaxations for SC/ST/OBC/women/PD will be given as per the GoI rules. Working/research experience in the field of **Device Simulations, TCAD tools and Python Programming** will be given preference. The selected candidates may **get an opportunity to enroll in PhD program** of the department after due process.

Interested and eligible candidates may appear for the **WALK-IN INTERVIEW (Hybrid Mode)** on **25th June 2022 at 11:00 am** in Block-3, Room Number 313, EED, IIT Patna for in-person, and **for online mode candidates need to fill the Google form linked here <https://forms.gle/tpk8KEZsBamq54as7>** with updated resume along original copies of all supporting documents (certificates, mark-sheets, and degrees). No TA/DA is admissible for appearing in the interview.

For any query contact Investigator(s) : **Dr. Jawar Singh (jawar@iitp.ac.in)**, or **Dr. Pramod Kumar Tiwari (pktiwari@iitp.ac.in)** Dept. of Electrical Engineering, IIT Patna.

Deputy Registrar

Copy to:

1. Associate Dean, R&D, IIT Patna
2. Advertisement file
3. Project file



INDIAN INSTITUTE OF TECHNOLOGY PATNA
BIHTA PATNA-801106
RESEARCH & DEVELOPMENT UNIT

PROJECT CODE: R&D/SP/EE/SERB/2021-22/624

ADVERTISEMENT NO: R&D/624/SERB/232

DATED: 13.06.2022

FORMAT OF APPLICATION FOR “JUNIOR RESEARCH FELLOW”

Name & Address Including email id and Phone no. (for Correspondence)	Category (GEN/OBC/SC/S T/PD)	DOB dd/ mm/yy	Professional Exam. (GATE/CSIR-NET, etc) & Validity
NAME IN CAPITAL Address: Phone: Email:			
Educational Qualification			
Institute/ Board	Exam Passed	Year of Passing	% of Marks/CPI
	10 th Class		
	12 th Class		
	Bachelors (B.Sc/B.Tech/B.E./BCA) or equivalent		
	Masters (M.Sc/M.Tech/M.E/MCA/ MA) or equivalent		

Qualifying degree	Degree/ major/Specialization
(B.Sc/B.Tech/B.E./BCA)	
(M.Sc/M.Tech/M.E/MA/MCA)	
Others	

Signature of applicant

Date:

Place: